



SRI KRISHNA COLLEGE OF TECHNOLOGY

(An Autonomous Institution | Affiliated to Anna University and Approved by AICTE
and NAAC – UGC)

KOVAIPUDUR, COIMBATORE - 641042.

DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

DETAILS OF COMPLETED RESEARCH (Faculty with Ph.D)

S. No.	Name	Title	Domain	Ph.D Awarded
1	Dr. R. Maheswar	Performance Analysis of an Energy Optimization Scheme for Cluster based Heterogeneous Sensor Networks	Wireless Sensor Networks	2012
2	Dr. S. Malathy	An Acknowledgement Based Ids with Random Key Generation for MANET	Wireless Communication	2013
3	Dr. R. Udaiyakumar	Certain Investigations on Low Power Design Technique for Basic Logic Element Architecture of Field Programmable Gate Arrays	VLSI	2014
4	Dr. R. Vadivelu	Certain Investigations on Spectrum Sensing Techniques Based on Transmitter Detection for Cognitive Radio	Wireless Communication	2015
5	Dr. Senoj Joseph	Efficient Algorithm for Online Task Placement on Dynamic Partially Reconfigurable FPGA	VLSI	2015
6	Dr. S. Vimalraj	Certain design and Analysis techniques for testing VLSI Circuits	VLSI	2016
7	Dr . A. Mithila	Design and Implementation of Voltage mode CMOS analog neuron using pulse stream	VLSI	2018

		technique		
8	Mr. S. Karthikeyan	Certain investigations for improving the performance of stroke segmentation and detection techniques on brain MRI images.	Medical Image Processing.	2018
9	Mr. P. Jayarajan	Energy Minimization and Performance Evaluation at Wireless Sensor Networks	Wireless Sensor Networks	2018
10	Mr. K. Srinivasan	Multiple moving objects tracking in a video using fuzzy sets	Video processing	Awaiting for Viva
11	Mr. V. Sivasankaran	Certain Investigation On Performance Improvement Of Handover Decision Making	Mobile communication handoff	Awaiting for Viva

DETAILS OF ONGOING RESEARCH (about to complete)

S. No.	Name	Title	Domain	Reg. Year
1	Ms. A. Kirthika	VLSI Architecture for Image Processing	VLSI	OCT 2009
2	Ms. Yamini shanmugam	Investigations on minimization of power in certain arithmetic digital CMOS VLSI circuits	VLSI	OCT 2010

DETAILS OF ONGOING RESEARCH (Pursuing)

S. No.	Name	Title	Domain	Year
1	Mr. U. Venkateshkumar	Spectrum Sensing in Cognitive Radios	Wireless Communication	JULY 2011
2	Mr. M. R. Thiyagu Priyadharsan	Hyper Spectral Image Processing	Image Processing	JULY 2011
3	Ms. G. Anitha	Efficient energy utilization in Wireless Sensor Networks	Wireless Sensor Networks	JULY 2012
4	Mr. S. Logesh kumar	Analysis and Reactable of MEMS based Devices.	Micro Electronics	JULY 2014
5	Ms. K. Divya	Performance analysis in Underwater Wireless Sensor Networks	Wireless Sensor Networks	JULY 2016
6	Ms. B. Banu selvasaraswathy	Human Activity Monitoring using Wireless Sensor Networks with	Wireless Sensor Networks	JULY 2016

		reduced SAR		
7	Mr. R.R. Thirrunavukkarasu	Denoising of ECG signal and Implementation in FPGA.	Signal Processing	JAN 2016
8	Mr. M. Navin Kumar	Performance Optimization in Digital and Mixed VLSI circuits	Low power VLSI testing	JAN 2017
9	Ms. T. Janani	Reconfigurable antenna for Wireless Sensor Networks	Antennas	JULY 2017
10	Mr. R. Vigneswaran	Design of Space Division multiplexing using few mode fiber.	Optical Communication	JULY 2017

DETAILS OF ONGOING RESEARCH (Recently Registered)

S. No.	Name	Title	Domain	Year
1	Mr. S. Ganesh Prabhu	An Efficient Implementation of Internet of Things for Embedded System Applications	Embedded System	JAN 2018
2	Mr. Z. Ahamed Yasar	Object Detection and Classification with Machine Learning	Machine Learning	JAN 2018
3	Ms. N. Agnes Shiny Rachel	Design and analysis for clock gating element for energy efficient implementation on on FPGA	VLSI	JAN 2018