

Dr.M.Karthigai Pandian., M.E., Ph.D.

Designation : Associate Professor

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Teaching Experience : 12 years

Qualification : Ph.D

Special Areas of Interest : VLSI Device Modelling, Nano Technology, Analytical and Biomedical Instrumentation

Workshops / Seminars / Conferences organized	
Title	Funded By
National Conference “Einsteinium 2017” at Einstein College of Engineering, Tirunelveli	ECE Association, Einstein College
Workshop on “Arm Processor and its Applications” at Einstein College of Engineering, Tirunelveli	ECE Association, Einstein College
Workshop on “Image Processing using MATLAB” at Einstein College of Engineering, Tirunelveli	ECE Association, Einstein College
Workshop on “Android Applications Development” at Einstein College of Engineering, Tirunelveli	ECE Association, Einstein College
Workshop on “In Vehicle Embedded Systems” at Kongu Engineering College, Perundurai	CSIR

Continuing Education Programmes	
PROGRAMME	HELD AT
One day workshop on “Embedded System using ATmega 16 Microcontroller”	Karunya University, Coimbatore.
Two days workshop on “Introduction to TCAD and its Applications”	SSN College of Engineering, Chennai
Three days workshop on “Future of Nano Electronics – Research and Challenges Ahead”	SKP Engineering College, Tiruvannamalai
Two days workshop on “3-D System design and Device Modeling”	Thiagarajar College of Engineering, Madurai
One day Workshop on “Research Issues in Electrical Engineering”	Velammal College of Engineering and Technology, Madurai
One day workshop on “IPR in Agriculture and Engineering Research”	Kongu Engineering College, Perundurai
Two days seminar on “In Vehicle Embedded Systems”	Kongu Engineering College, Perundurai
Two weeks FDP on “Entrepreneurship”	PSG College of Technology, Coimbatore
One week Industrial Training at Chettinad Cement Corporation Limited	CCCL, Dindigul

One day workshop on “Advances in Process Control and Instrumentation”	St.Joseph’s College of Engineering, Chennai
One day seminar on “Environment Science and Engineering”	RVS College of Engineering and Technology, Dindigul
Two weeks FDP on “Adaptive Signal Processing and their applications to Speech Processing”	RVS College of Engineering and Technology, Dindigul
Course materials prepared	
Electronic Circuits – I	
Medical Electronics	
Measurements and Instrumentation	
Communication Theory	

Paper Publications (Conferences)	
TITLE	CONFERENCE
Color Image Compression using SOFM	National Conference Exotech ‘06 held at Sri Subramanya College of Engineering and Technology, Palani.
Technology Assessments of Hybrid Approaches to the Fabrication of High Performance Silicon Nanowire Transistors	National Conference ETWT ‘10 held at Thiagarajar College of Engineering, Madurai
Synthesis, Fabrication Techniques and Applications of High Performance Silicon Nanowire Transistors	International Conference ICMAT ‘11 held at NUS, Singapore
2D Analytical Surface Potential Model of Multi-gate Silicon Nanowire Transistors for Biosensor Applications	International Conference ICMAT ‘11 held at NUS, Singapore
High Performance Double Gate Silicon Nanowire Transistors	International Conference ICEVENT ‘13 held at SKP Engineering College, Thiruvannamalai
Performance Analysis and Threshold Voltage Modeling of Surrounding Gate Silicon Nanowire Transistors	International Conference ICEVENT ‘13 held at SKP Engineering College, Thiruvannamalai
An Overview of Various Threshold Voltage Modeling Approaches in Surrounding Gate Silicon Nanowire Transistors	National conference NCETET ‘13 held at Pandian Saraswathi Yadav Engineering College, Sivagangai
A Review on Modeling of Double Gate Silicon Nanowire Transistors	National conference NCETET ‘13 held at Pandian Saraswathi Yadav Engineering College, Sivagangai
2D Compact Model of a Surrounding Gate Silicon Nanowire Transistor	International conference ICETET ‘14 held at Pandian Saraswathi Yadav Engineering College, Sivagangai

Paper Publications (Journals)	
TITLE	Journal
Potential and Quantum Threshold Voltage Modeling of GAA Nanowire MOSFETs	International Journal of Active and Passive Electronic Components
Analytical Threshold voltage Modeling of Silicon Nanowire MOSFETs with different geometries	International Journal of Electrical Engineering and Technology
Analytical Modeling of Junctionless Surrounding Gate Silicon Nanowire Transistors	International Journal of Nanoelectronics and Optoelectronics
Analytical I-V Modeling of Accumulation Mode Cylindrical Surrounding Gate Silicon Nanowire MOSFETs	International Journal of Applied Engineering Research
Comparative Performance Analysis of Multi Gate Tunnel Field Effect Transistors	International Journal of Nano Research

ACCOLADES
Recognized by Anna University as Research Supervisor in the Department of Information and Communication Engineering (ICE)
Awarded as the BEST STAFF in the EIE department at Kongu Engineering College, Perundurai for the academic year 2007-08